



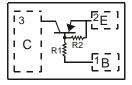


## PRE-BIASED SMALL SIGNAL SURFACE MOUNT PNP TRANSIS

## **Features**

- **Epitaxial Planar Die Construction**
- Ultra-Small Leadless Surface Mount Package
- Ideally Suited for Automated Assembly Processes
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability



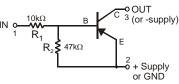


Top View

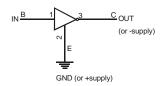
## **Mechanical Data**

- Case: DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Collector Dot (See Diagram and Marking Information)
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.0009 grams (approximate)

DFN1006-3



Schematic and Pin Configuration



Equivalent Inverter Circuit

# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	-50	V
Input Voltage	V <sub>IN</sub>	+6 to -40	V
Output Current	Io	-70	mA
Output (Collector) Current	I <sub>c(max)</sub>	-100	mA

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @T <sub>A</sub> = 25°C	$P_{D}$	250	mW
Power Derating above 25°C	P <sub>der</sub>	2	mW/°C
Thermal Resistance, Junction to Ambient Air (Note 3) @T <sub>A</sub> = 25°C (Equivalent to one heated junction of PNP)	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	$T_j$ , $T_{STG}$	-55 to +150	°C

# Electrical Characteristics: Discrete PNP Transistor (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Off Characteristics (Note 4)						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-50	_	_	V	$I_C = -10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	-50	_	_	V	$I_C = -1.0 \text{mA}, I_B = 0$
Collector-Base Cut Off Current	I <sub>CBO</sub>	_	_	-0.1	μΑ	$V_{CB} = -50V, I_{E} = 0$
Collector-Emitter Cut Off Current, IO(OFF)	I <sub>CEO</sub>	_	_	-0.5	μΑ	$V_{CB} = -50V, I_B = 0$
Emitter-Base Cut Off Current	I <sub>EBO</sub>	_	_	-0.2	mA	$V_{EB} = 4V, I_{C} = 0$
Input Off Voltage	V <sub>I(OFF)</sub>	_	_	-0.3	V	$V_{CC} = -5V, I_{O} = -100uA$
On Characteristics (Note 4)						
Input-On Voltage	$V_{I(ON)}$	-1.4	_	_	V	$V_O = -0.3V$ , $I_O = I_C = 1mA$
Input Current	II	_	_	-0.88	mA	$V_I = -5V$
DC Current Gain	h <sub>FE</sub>	80	_	_	_	$V_{CE} = -5V$ , $I_C = -5mA$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	-0.25	V	$I_C = -50 \text{mA}, I_B = -2.5 \text{mA}$
Output On Voltage	V <sub>O(ON)</sub>	_	-0.1	-0.3	V	$I_1 = -0.25 \text{mA}, I_0 = -5 \text{mA}$
Input Resistance	R1	7	10	13	ΚΩ	_
Resistance Ratio	(R2/R1)	3.7	4.7	5.7	_	_
Small Signal Characteristics						
Current Gain-Bandwidth Product	f <sub>T</sub>	_	250	_	MHz	$V_{CE} = -10V$ , $I_{E} = -5mA$ , $f = 100 MHz$

Notes:

- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
   Device mounted on FR-4 PCB, 1" x 0.85" x 0.062"; pad layout as shown on page 3 or Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Short duration pulse test used to minimize self-heating effect. Pulse width tp<300µS, Duty Cycle, d≤2%.



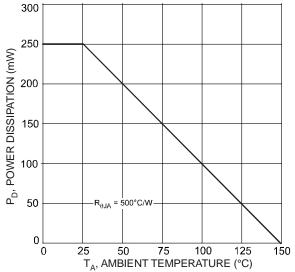


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

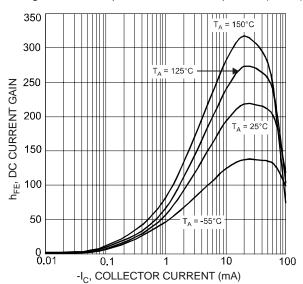


Fig. 3 Typical DC Current Gain vs.
Collector Current

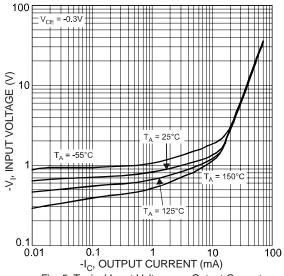


Fig. 5 Typical Input Voltage vs. Output Current (On Characteristics)

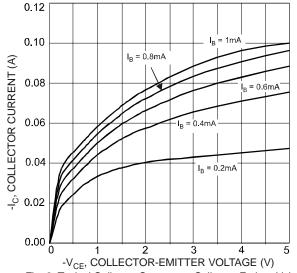


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

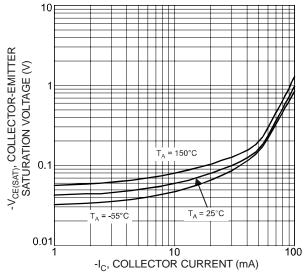


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

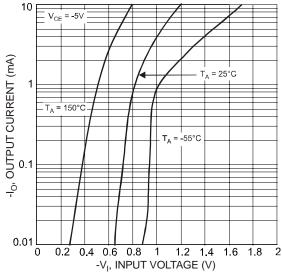


Fig. 6 Typical Output Current vs. Input Voltage (Off Characteristics)

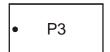


# **Ordering Information** (Note 5)

Device	Packaging	Shipping
DDTA114YLP-7	DFN1006-3	3000/Tape & Reel

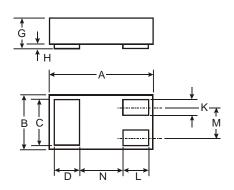
Notes: 5. For packaging details, please go to our website at http://www.diodes.com/ap02007.pdf.

# **Marking Information**



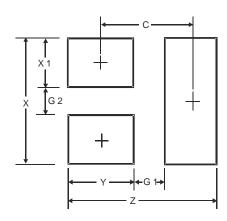
P3 = Product Type Marking Code Dot Denotes Collector, Pin 3

# **Mechanical Details**



DFN1006-3				
Dim	Min	Max	Тур	
Α	0.95	1.05	1.00	
В	0.55	0.65	0.60	
С	0.45	0.55	0.50	
D	0.20	0.30	0.25	
G	0.47	0.53	0.50	
Н	0	0.05	0.03	
K	0.10	0.20	0.15	
L	0.20	0.30	0.25	
М	_	_	0.35	
N	_	_	0.40	
All Dimensions in mm				

# **Suggested Pad Layout**



Dimensions	Value
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7

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